

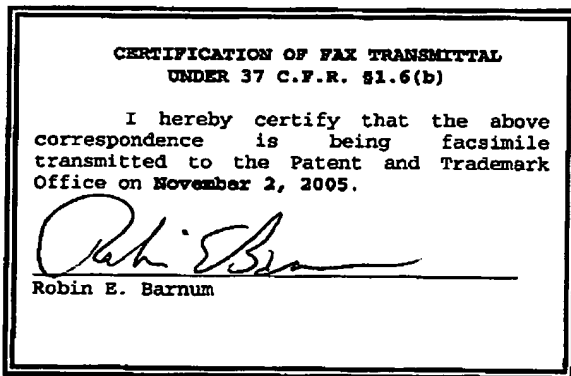
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Swoboda et al Art Unit: 2123
Serial No.: 09/943,598 Examiner: Russell L. Guill
Filed: August 30, 2001 Docket: TI-30480
For: SYNCHRONIZING ON-CHIP DATA PROCESSOR TRACE AND TIMING
INFORMATION FOR EXPORT

AMENDMENT UNDER 37 C.F.R. 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated July 25, 2005. A petition and fee for a one month extension of time is attached. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: